

PATENT Customer No. 22,852 Attorney Docket No. 04329.2210

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:) .	
Katsuhiko HIEDA, et al.) Group Art Unit: 2811	76 2800 10 2800
Serial No.: 09/469,190) Examiner: Vu, Hung K.	90 3. T.
Filed: December 21, 1999	<i>)</i>)	L Ro
For: SEMICONDUCTOR DEVICE AND METHOD FOR MANUFACTURING THE SAME)))	ROOM
Assistant Commissioner for Patents Washington, DC 20231		
Sir:		

PRELIMINARY AMENDMENT

Applicants submit this Preliminary Amendment along with a Continuing Prosecution Application (CPA). Prior to the examination of the above application, please amend this application as follows:

IN THE CLAIMS!

Please cancel claims 11 - 14 and 16 without prejudice or disclaimer of the subject matter therein, amend claims 10, 15, 17, and 18, and add new claims 25 - 30, as follows:

- FINNEGAN HENDERSON FARABOW GARRETT& DUNNER LLP
- 1300 I Street, NW Washington, DC 20005 202.408.4000 Fax 202.408.4400 www.finnegan.com
- 10. (Twice Amended) A semiconductor device comprising:
- a semiconductor substrate,
- a conductive plug formed on the semiconductor substrate;
- a capacitor lower electrode including a bottom portion formed on the conductive plug and a cylindrical-type wall contacting the bottom portion to surround the bottom portion, the